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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/785,671	02/24/2004	Hiroyuki Nakajima	17472	2687

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EXAMINER

JOHNSON, BRIAN P

ART UNIT PAPER NUMBER

2183

DATE MAILED: 09/21/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/785,671	Applicant(s) NAKAJIMA, HIROYUKI	
	Examiner Brian P. Johnson	Art Unit 2183	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 June 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-12 and 20-37 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☒ Claim(s) 1-12 and 20-37 are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

1. Claims 1-12 and 20-37 are presented for examination.
2. Restriction to one of the following inventions is required under 35 U.S.C. 121:
 - I. Claims 1-12 and 21-37, drawn to a processor, classified in class 712, subclass 209.
 - II. Claims 20, drawn to a process for creating a processor, classified in class 716, subclass 18.
3. The inventions are distinct, each from the other because of the following reasons.

Inventions I and II are restrictable under MPEP 806.05(f) if one or more of the following can be shown: (A) that the process as claimed is not an obvious process of making the product and the process as claimed can be used to make another materially different product; or (B) that the product as claimed can be made by another materially different process. See MPEP § 806.05(f).
4. With regards to requirement (B), Applicant claims a method involving separating a design into hardware and software components of the LSI circuit. Examiner asserts that the processor of invention I could be created through other means. For example, the processor of invention I could be created by hand, at the transistor level instead of large scale integration. This process distinctly different process could be done to create invention I.
5. With regards to requirement (A), the process described in invention II could be used to create a completely different product. For example, the ideas of separating the hardware and software of a design, and utilizing behavioral synthesis could be used to create any software. Claim 20, arguably a linking claim, appears to give information regarding a decoder and execution unit, but these claims are so broad that, again, most any processor could be created by

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this method. Examiner asserts that most every processor contains these mechanisms. The remaining dependant claims for invention II give slightly more specific information, but in no way limit the process to the creation of invention I.

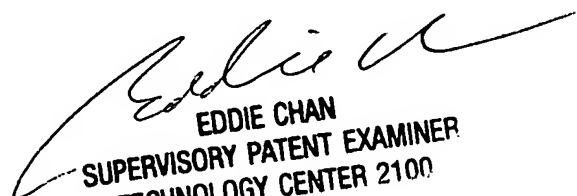
6. Because these inventions are distinct for the reasons given above and the search required for any one of group I and II is not required for any of the other groups, restriction for examination purposes as indicated is proper.

7. Applicant is advised that the response to this requirement to be complete must include an election of the invention to be examined even if the requirement is traversed (37 CFR § 1.143).

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian P. Johnson whose telephone number is (571) 272-2678. The examiner can normally be reached on 8-4:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on (571) 272-4162. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


EDDIE CHAN
SUPERVISORY PATENT EXAMINER
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